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P+ N+ P N P+ N+ P Aluminum is deposited on the wafer. Photoresist is then spun onto the wafer and mask #6 is used to define the wiring regions.

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Material and Process Limits in Silicon VLSI Technology

JAMES D. PLUMMER, FELLOW, IEEE, AND PETER B.

GRIFFIN Invited Paper The integrated circuit (IC) industry has followed a steady path of shrinking device geometries for more than 30 years. It is widely believed that this process will continue for at least another ten years.

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Saddle River, NJ. By Plummer, Deal and Griffin P N+ N P+

P+ The P+ isolation regions are driven down to the P

substrate to laterally isolate the devices. SiO₂ is grown on the

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surface during this drive-in. Note that the buried layer

S G D S G D N P N N P N P WELL P+

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